

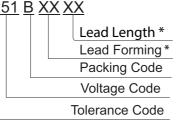


INTRODUCTION

Radial Leaded Multilayer Ceramic Capacitors are made with a superior epoxy coating for moisture and mechanical protection. The small size is suitable for a wide range of applications, including: data processing, telecommunications, instrumentation, and industrial controls.

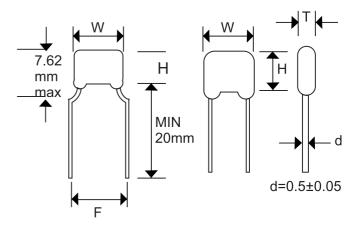
PART NUMBER EXAMPLE RD 25 ZU 104 M 251 B XX XX Series Size code Temperature

Capacitance



FEATURES

- Epoxy Coating
- Minature Size
- Auto Insertable
- Reliable



*omit for standard leads and taped product

■ SIZE CODE & CAPACITANCE RANGE

Size	Dime	nsions	(mm)	Voltage	Voltage	X7R	X5R	Y5V	Z5U
Code	W(max)	H(max)	T(max)	Vdc	Code	(µf)	(µf)	(µf)	(µf)
				6.3V	060		1.50 ~ 10.00	4.70 ~ 22.00	
				10V	100		0.33 ~ 4.70	2.20 ~ 10.00	
				16V	160	0.22 ~ 2.20	0.15 ~ 2.2	1.00 ~ 4.70	
RD2 *	4.0	4.0	2.5	25V	250	0.10 ~ 1.50	0.10 ~ 1.0	0.47 ~ 2.20	0.47 ~ 2.20
				50V	500	0.00022 ~ 0.33	0.10 ~ 1.00	0.1 ~ 1.00	0.1 ~ 1.00
				100V	101	0.00022 ~ 0.10			
				250V	251	0.001 ~ 0.033			
				6.3V	060		10.00 ~ 22.00	47 ~ 100.0	
				10V	100		6.80 ~ 10.00	22~47.0	
				16V	160	0.30 ~ 4.70	1.5 ~ 10.00	10 ~ 22	
RD3 *	5.1	5.1	4.0	25V	250	0.68 ~ 2.20		4.7 ~ 10	
	5.1	5.1	4.0	50V	500	0.47 ~ 1.00		2.2 ~ 4.7	
				100V	101	0.033 ~ 0.47			
				250V	251	0.015 ~ 0.15			
				500V	501	0.001 ~ 0.033			

* 2 = 2.5mm ± 1mm Lead Spacing, F Dimension

* 5 = 5.0mm ± 1mm Lead Spacing, F Dimension

Note: Contact RFE for capacitance and voltage combinations not shown above.

CAPACITANCE CODE EXAMPLES

Code	1R0	100	330	221	102	222	103	104	224	105
Capacitance in pF	1.0pF	10pF	33pF	220pF	1,000pF	2,200pF	10,000pF	100000pF	220,000pF	
Capacitance in µF				0.00022µF	0.001µF	0.0022µF	0.01µF	0.1µF	0.22 µF	1.0µF





30mm ± 1.0MAX

\$370mm

REEL-PACK

MAX

TOLERANCE CODE

Code	Cap. Tol.			
С	±0.25pF			
D	±0.5pF			
F	±1%			
G	±2%			
J	±5%			
К	±10%			
М	±20%			
Z	±80%, -20%			

PACKAGING CODE

Style

BULK

AMMO

REEL

Quantity

1000

3000

3000

Code

В

А

R

Code

KO

KI

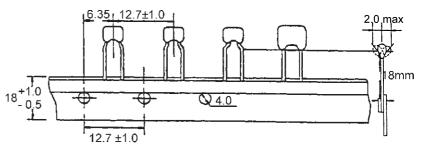
LEAD FORMING

TEMPERATURE COEFFICIENT

Code	Temp. Charact.	Temperature Range	Capacitance Change
CG	C0G/NPO	-55 ~ 125℃	0±30 ppm/°C
X5R	X5R	-55 ~ 85℃	±15%
XR	X7R	-55 ~ 125℃	±15%
YV	Y5V	-30 ~ 85°C	+22%, -82%
ZU	Z5U	+10~85°C	+22%, -56%

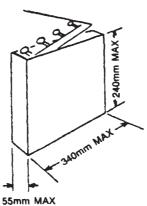
See other RD Series for COG/NPO

TAPING & PACKAGING



CASSETTE

REEL



■ LEAD LENGTH EXAMPLE (BULK ONLY)

Stand off forming

Kink Out

Kink In

Code	20	4.0	4.5	5.0	5.5
Length (mm)	standard	4±1	4.5±1	5±1	5.5±1





ELECTRICAL CHARACTERISTICS

C0G/NPO

Parameter	Specification	Measuring	g Condition	
Capacitance Q	with the specified tolerance C ≥ 30pF:Q C < 30pF:Q ≥ 1000 C<30pF:Q≥400+20xC (C is nominal capacitance)	Shall be measured at 25°C ± 2° C≤1000pF@1MHz±20%, 1±0.2° 1±0.2Vrms		
Withstanding Voltage	no defects	Applied Voltage: Rated Voltage 100V~500V Rated voltage (ove Duration: 1 to 5 sec. The charge/discharge current is	r) x 1.5	
Insulation Resistance	More than 10GΩ or 500MΩ0µr (whichever is less) 16Vdc product: More than 10GΩ or 100MΩ0µr (whichever is less)	Apply rated voltage for 1 minute 70%R.H.max. 16Vdc product: N	e at 25°C ± 2°C and	
		Resistance to Soldering Heat	Thermal Shock	
ΔC	±2% or ±0.25pF (whichever is greater)	The lead wire is immersed in the from the main body at $260 \pm 5^{\circ}$	_	
Q	C ≥ 30pF:Q ≥ 1000 C<30pF:Q≥400+20 x C (C is nominal capacitance)	Let sit at room temperature for 24 ±2 hours, then measure	Perform the five cycles according to the four heat treatments listed in the following table. Remove and	
I.R.	More than 1000MΩ or 50MΩ0µr (whichever is less) 16Vdc product: More than 1000GΩ or 10MΩ0µr (whichever is less)	Perform the initial measurement	let sit at room temperature for 24±2hours, then measure. [See Table A, below]Perform the initial me asurement.	
		Moisture Resistance (steady state)	High Temperature Loading	
ΔC	$\pm 2\%$ or $\pm 0.25 pF$ (whichever is greater)	The lead wire is immersed in the from the main body at $260 \pm 5^{\circ}$		
Q	C ≥ 30pF:Q ≥ 1000 C<30pF:Q≥400+20 x C (C is nominal capacitance)	Let sit at room temperature for 24 ±2 hours, then measure	Perform the five cycles according to the four heat treatments listed in the following table. Remove and	
I.R.	More than 1000MΩ or 50MΩ0μr (whichever is less) 16Vdc product: More than 1000GΩ or 10MΩ0μr (whichever is less)	Perform the initial measurement	let sit at room temperature for 24±2hours, then measure. [See Table A, below] Perform the initial measurement.	
		Resistance to Soldering Heat	Thermal Shock	
ΔC	$\pm 2\%$ or $\pm 0.25 pF$ (whichever is greater)	The lead wire is immersed in the from the main body at $260 \pm 5^{\circ}$	C for 10±0.5 sec.	
Q	C ≥ 30pF:Q ≥ 1000 C<30pF:Q≥400+20 x C (C is nominal capacitance)	Let sit at room temperature for 24 ±2 hours, then measure	Perform the five cycles according to the four heat treatments listed in the following table. Remove and	
I.R.	More than 1000MΩ or 50MΩ0µr (whichever is less), 16Vdc product: More than 1000GΩ or 10MΩ0µr (whichever is less)	Perform the initial measurement	let sit at room temperature for 24±2hours, then measure. [See Table A, below] Perform the initial measurement.	

• Withstanding voltage: No defects

• Exterior: No abnormalibis

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	Step	1	2	3	4
Table A	Temp. (℃)	Min. Operating Temp.	Room Temp.	Max. Operating Temp.	Room Temp.
	Time 30±3		15	30±3	15

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ELECTRICAL CHARACTERISTICS

X7R/X5R/Y5V/Z5U

Parameter		Specificatio	n			Measuring Condition				
Capacitance	with t	he specified tolerance								
	25V min	X7R=0.03max X7R=0.055max (C≥ 1.0μF) Y5V=0.075max								
Dissipation Factor (tanδ)	X/R/X5R=0.05max 10V Y5V=0.10max						Shall be measured at 25°C± 2°C at the frequency and voltage _X7R/X5R/Y5V @ 1Hz ± 10%, 1 ± 0.2Vrms Z5U @ 1Hz ± 10%, 0.5 ± 0.2Vrms			
	International X7R/X5R=0.05max max Y5V=0.125max 6.3V X5R=0.075max					-	-			
Withstanding Voltage	no de					100V Dura	-500V rated tion: 1 to 5			
nsulation Resistance		than 10GΩ or 500MΩ0μF (whic ict: More than 10GΩ or 100MΩ0		,				ge for 1 minul ement voltage	te at 25°C± 2°C and 70% R.H. max 16Vd e is 25Vdc	
	Temp	erature Compensating				Resi	stance to S	oldering Hea	t Thermal Shock	
ΔC	X7R/X5R= ±7.5% Y5V=±20% Z5U=±20%					The lead wire is immersed in the melted solder 1.5mm to 2mm from the main body at $260 \pm 5^{\circ}$ C for 10 ± 0.5 sec.				
	25V min	X7R=0.03max X7R=0.055max (C≥ 1.0µF) Y5V=0.075max								
D.F.	16V Z5U=0.05max X7R/X5R=0.10max								Perform the five cycles according to th four heat treaments listed in the Table	
	10V max	X7R/X5R=0.05max Y5V=0.125max				150°C± 10°C for 1 hours			(below). Remove and let sit at room temperature for 48 ±4 hours, then measure.	
I.R.	More	X5R=0.075max than 10GΩ or 500MΩ0μF (whic					surement.			
1.1X.	•	ict: More than 10GΩ or 100MΩ()µF (whicl	hever is less)		Moisture Resistance			High Temperature Loading	
	-	X5R= ±15%					(steady	state)		
ΔC	Y5V= Z5U=	±30%						OC voltage at 0 to 95%	Apply the 200% of rated DC voltage for 1000± 48 hours at the maximum operating temperature at ±2°C. Remov	
		X7R=0.03max X7R=0.055max (C≥ 1.0μF) Z5U=0.09max X7R/X5R=0.05max				R.H. for 500 +24 -0 hrs. Remove and let sit at room		-0 hrs. sit at room 18 +4 hrs.	and let sit for 48±4 hours then measur The charge/discharge current is less than	
D.F.	10V Y5V=0.10max x7R/X5R=0.05max Y5V=0.10max y5V=0.125max Y5V=0.125max				a heat treatment at 150°C± 10°C for 1 hour.Apply 200% of the rated DC volta 1 hour at the maximum operating temperature ± 2°C. Remove let si room temperature for 48 ± 4 hour			Apply 200% of the rated DC voltage for 1 hour at the maximum operating temperature ± 2°C. Remove let sit at		
	6.3V X5R=0.075max									
I.R.		than $10G\Omega$ or $50M\Omega0\mu$ F (which inct: More than $1000G\Omega$ or $10M\Omega$			5)				for 100V~500V	
		Table B	Step Temp. (°C)	1 Min. Operating	Ro	2 oom mp.	3 Max. Operating	4 Room Temp.		
			Time	Temp. 30±3		5	Temp. 30±3	15		

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STORAGE

- 1. The storage conditions < 40°C, < 70% R.H.
- 2. After opening the package, please store in desiccators.

ENVIRONMENTAL & TEST CHARACTERISTICS

Parameter	Specification	Measuring Condition
Strength of termination	Termination not to be broken or loosened Force : 2 LB min. Keep time : 10±1 sec.	F,
Solderability of leads	Lead wire to be soldered vertically up to the coating end point. At least 75% of lead surface is covered	Solder temperature: 260 ± 5°C Dipping: 2 ± 0.5 sec. (Containing Ag 2~5%) (Flux shall be used)