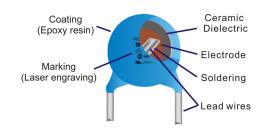




INTRODUCTION

- Complies with IEC60384-14
- Single Layer AC Disc Safety Capacitor
- Coated with flame-retardant epoxy resin (conforming to UL94V-0)
- Lead with tin plated copper wire, inert metal copper or silver as a coating after the electrode, see diagram
- Halogen-free available



■ RECOGNITIONS: made for RFE by UL/VDE/CQC shop Welson

Approved	Country	Standards	Rated \	/oltage	Certification Number		
monogram	Country	Stariuarus	KL (X1Y2)	WD (X1Y1)	KL (X1Y2)	WD (X1Y1)	
c 911 us	USA	UL 60384-14	X1 : 500Vac	X1 : 760Vac	F404570		
c 71 Us	Canada	UL 60304-14	Y2 : 500Vac	Y1 : 500Vac	E104572		
© ^V E ■ X 10			X1 : 440Vac Y2 : 300Vac	X1 : 660Vac Y1 : 500Vac	40016156	4016157	
	Korea	KC60384-14 / KC60384-1	250Vac		SZ03004-17002	SZ03004-17001	
cec	China	GB/T 6346.14-2015	X1 : 400Vac Y2 : 250Vac	X1 : 400Vac Y1 : 250Vac	CQC03001008380	CQC03001008379	

■ ELECTRICAL CHARACTERISTICS

Characteristics	WD type (X1 Y1)	KL type (X1 Y2)				
Capacitance range	1pF to 0.01uF	1pF to 0.015uF				
Rated voltage	X1: 760/660/400Vac; Y1: 500/250Vac	X1: 500/440/400Vac; Y2: 500/300/250Vac				
Dielectric strength	4000Vac (50Hz-60Hz, 50mA max.) for 1 minute.	2500Vac (50Hz-60Hz, 50mA max.) for 1 minute.				
Capacitance (CR)	Within the specified tolerance. Y5P, Y5U, Y5V, X7R measured at 1kHz±20% C0G, SL measured at 1MHz±20% Both are 1Vrms, 25					
Dissipation Factor (tanδ) or Q Value	:	Y, Y5U, X7R: Y5V: nδ: 0.025 max. tanδ: 0.050 max.				
Insulation resistance	10,000MΩ minimum at 500VDC for 1 minu	ute.				
Operating temperature	-55°C to 125°C					





■ CAPACITANCE CHART

Class				X1 Y2	(KL type)					X1 Y	1 (WD ty	pe)		
Dielectric		COG (C)	SL (L)	Y5P (B)	X7R (X)	Y:	5U E)	Y5V (F)	COG (C)	SL (L)	Y5P (B)	X (7R X)	Y5U (E)	Y5V (F)
apacitance pF)	1.0 1.5	G G	G G	G G					G G	G G	G G				
	2.2 3.3	G G	G G	G G					G G	G G	G G				
	4.7 5.1	G G	G G	G G	G				J	J	1		1		
	6.8	J	J G	1	G			-	K	K G	J		J		
	12 15		G G	1	G G					G G	J		J		
	18 20 22		G G	1	G G					G G	J		J J		
	27 30		J	1	G G					G G	J		K K		
	33 36		J	- 1	G					G G	K		K K		
	39 47		J K	1	G G					G L	K		K K		
	56 68 82		K K K	K K	G G					L L	K K		K K		
	100 120 150			G G	G G						K K		K K	G G	
	180 220			G G	G		\top				K		K K	G G	
	270 330			G	G		Ē				K		K	G	
	390 470			J	1	-		E			K		K K	G G	G
	560 680 820			J	l l J			E E			K		K L L	G G	G G
	860 1000 1200			J L L	J J M	(E 3	E G G			L N N		L N	1 1	G G
	1500 1800			N N	M			1			O R		O R	K	J
	2000 2200 2700			0			J	 			R		R R	L L	J K
	2800 3000			4.5	- 0		L I	J				+		L M	K L
	3200 3300							J J		D L	ļ	 T -		M M	L L
	3600 3900					1	N N	K K	-					0	M
	4000 4700			17			N N	L	—	W E @ X1400750V-		-		Q Q	M
	5000 5500					1	N N	L	_ /	Y2 250 / 300V-		_		Q	N N
	5600 6000 6800					(M M			Φd			Q S S	N N N
	8000 8200					1	2 2	N N	_	F	-			S	Q
	8600 9000						S S	N O			-			S	R
	10000 15000						S	0 Q						Ü	R
Lead Spacing ((±1.0mm)				5.0 &	7.5 & 10.0						7	7.5 & 10.0			
Thickness (T max.				6	.0mm							6.0mm			
Capacitance Tolerance				Below 10	pF: ±0.25p	F or ±0.50)pF; 1	0pF~100p	F: ±5% or :	±10%;	Over 100p	F: ±10%	or ±20%		
Coating							E	poxy resir	n (UL94V-0)						
Body Color		Blue													
Code		E	G	Î.	J	к	L	М	N	0	Q	R	s	Т	U
Diameter (D)	max.	7mm	8mm	9mm	10mm	11mm	12mm	14mm	15mm	16mm	19mm	20mm	22mm	25mm	28n





■ HOW TO ORDER

<u>5 - KL F 472 M AC2K5 10 Y 5</u>
(1) (2) (3) (4) (5) (6) (7) (8)

1. Type Code: KL class X1-Y2; WD class X1-Y1

2. Temperature Characteristic:

Code	Temp. Coefficient	Code	Temp. Coefficient	Code	Temp. Coefficient	
С	C0G	В	Y5P	E	Y5U	
S	SL	Χ	X7R	F	Y5V	

3. Capacitance Code:

Expressed by three-digit alphanumeric. The unit is pico-farad (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers. If there is a decimal point in between first two figures, it is expressed by the capital letter "R". See below examples:

Code	Capacitance	Code	Capacitance	Code	Capacitance	
5R1	5.1 pF	100	10 pF	472	4700 pF	
8R0	8 pF	101	100 pF	103	0.1 uF	

4. Capacitance Tolerance Code

Code	Tolerance	Code	Tolerance	Code	Tolerance
С	± 0.25pF	J	± 5%	М	± 20%
D	± 0.50pF	K	± 10%	Z	+80/-20%

5. Minimum Test Voltage (AC): X1Y2 = AC2K5, X1Y1 = AC4KV

6. Lead Spacing (F)

Code	Lead spacing (F)	Code	Lead spacing (F)
5	5.0±0.8mm	7	7.5±0.8mm
6	6.35±0.8mm	10	10.0±0.8mm

7. Lead style

	Code	S	Υ	Χ	W	Z
<u>-</u>	Lead Style	D T	D I	D d	D T	D T

8. Lead length & package style:

Omitted for un-cut bulk pack or Code L

5 = 5 + /-1mm, bulk pack, 6 = 6 + /-1mm ... etc. (cut leads only available in bulk pack)

A = Taped & Ammo pack, R = Taped & on Reel



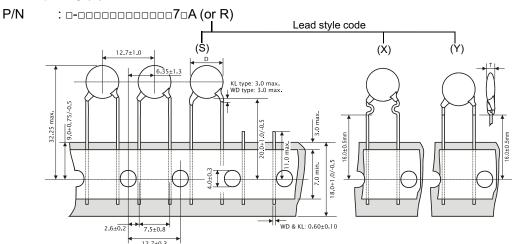


TAPING SPECIFICATION

P/N

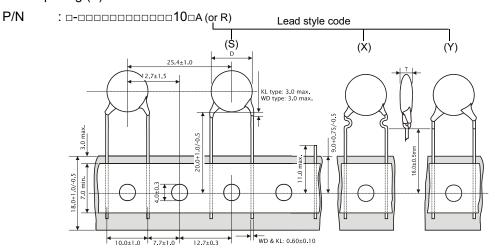
Lead spacing (F): 5mm

Lead spacing (F): 7.5mm



12.7±0.3

Lead spacing (F): 10.0mm



Page 4 of 8





SPECIFICATION AND TEST METHOD

- 1. Please measure with the ambient temperature of 25±2°C, relative humidity of 45~85 percent.
- 2. Please measure in this order: Capacitance, DF, IR, Test voltage
- 3. Measurement of voltage of high-voltage capacitors should be based on 150V/µs rate, rise from 0V voltage . The test of time can be increased to test the voltage start time. Measuring the end of the capacitor should discharge.

No.	lte	em	:	Specification		Te	sting Meth	od
1		ance and nsions		fect on appearance form ns are within specified range.		acitor should be v		d for evidence of defect. e calipers.
2	Mar	king	To be easily le	egible		The capacitor	r should be visua	ally inspected
3	Capacita	nce (CR)	Within specifie	ed tolerance				
4		on Factor r Q Value	Char. C, S X, B, E F	Specification $Q \geq 400+20CR \; (CR < 30pF)$ $Q \geq 1000 \qquad (CR \geq 30pF)$ $tan \; \delta : \; 0.050 \; max.$ $tan \; \delta : \; 0.075 \; max.$	The capacitor and dissipation factor should be me 25±1°C with 1±0.2KHz (char. C & L: 1±0.2MHz) at AC5V(r.m.s.) max.			
5	Insulation Resistance (I.R.)		10,000MΩ mir	ո.	The insulation resistance should be measure with DC500 within 60 ± 5 sec. of charging. The voltage should be applied the capacitor through a resistor of $1M\Omega$.			
		Between Lead Wires	No failure		Table 1	acitor should not bare applied betwee /Discharge curren Type Test Voltage	en the lead wire	en test voltages of s for 60 sec. WD AC4000V
6	Dielectric Strength	I I		First, the terminals of the capacitor should be connected together. Then, as shown in figure at right, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 4mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC voltage of Table 2 is applied for 60 sec. between the capacitor lead wires and metal balls.				
						_	< Table 2 >	W.D.
						Type Toot Voltage	KL A C2500V	WD
			Char.	Canacitanes Change		Test Voltage	AC2500V	AC4000V
			B Char.	Capacitance Change Within + 10%		acitance measure I in Table 3.	ement should be	made at each step
				Within ± 10% Within ± 15%			< Table 3 >	
			X E	Within +20% / -55%			- 1 4510 0	
	 .			Within +30% / -80%	1	Step	Temperatu	, ,
7		erature teristics	<u> </u>	p range: -25 to +85°C)	1	1	20 ± 2	
			(1811)	1	1	2	-25 ± 2	
			Char.	Temperature Coefficient	1	3	20 ± 2	2
			C s	0±30ppm/°C		4	85 ± 2	2
				+350 to -1000ppm/°C		5	20 ± 2	2
			(Tem	ip range: -25 to +85°C)				

continue ...





SPECIFICATION AND TEST METHOD (continue ...)

No.	Ite	em		Specification	Testing Method			
8	Solder of Le		coating on t	hould be soldered with uniform he axial direction over 3/4 of the tial direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder (Pb37/Sn63) 235±5°C			
		Appearance	No marked	defect	The lead wires should be immersed in solder of 350±10°C or 260±5°C up to			
9	Soldering Effect	Capacitance change	C, S: ±5% c B: ±10% X, E, F: ±20	r 1pF, whichever is larger %	1.5mm to 2.0mm from the root of terminal for 3.5±0.5 sec. (10±1 sec for 260±5°C)			
9	(Non-Preheat)	I.R.	1,000MΩ m	in	Pre-treatment: Capacitor should be stored at 85±2°C for 1hr., and then placed at room condition for 24±2 hrs. before initial meaurements.			
		Dielectric Strength	Per Item 6		Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.			
		Appearance	No marked	defect				
10	Soldering Effect	Capacitance change	C, S: ±5% c B: ±10% X, E, F: ±20	r 1pF, whichever is larger	Capacitor should be stored at 120+0/-5°C for 60+0/-5 sec. The lead wires should be immersed in solder of 260±5°C up to 1.5mm to 2.0mm from the root of terminal for 7.5+0/-1 sec.			
	(On-Preheat)	I.R.	1,000MΩ m	in	Pre-treatment and Post-treatment: see per Item 9			
		Dielectric Strength	Per Item 6		·			
		Appearance	No marked	defect	The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10Hz to 55Hz,1.5mm			
11	Vibration Resistance	Capacitance	Within the s	pecified tolerance	in total amplitude, with about a 1 minute rate of vibration change from 10Hz to 55 Hz and back to 10Hz. Apply for a total of 6 hrs.,			
		tan δ or Q	Per Item 4		2 hrs each in 3 mutually perpendicular directions.			
		Appearance	No marked	defect				
		Capacitance change	C: Within ±2 S: Within ±5 X, B, E: Wit F: Within ±1	5% hin ±10%				
12	Humidity (Under Steady State)	tan δ or Q	Char.	$\begin{tabular}{ll} Specification \\ Q \ge 275 + 5/2 C_R (C_R < 30 pF) \\ Q \ge 350 (C_R \ge 30 pF) \end{tabular}$	Set the capacitor for 500±12 hrs., at 40±2°C in 90 to 95% relative humidity. Post-treatment:			
			X, B, E	tan δ: 0.050 max. tan δ: 0.075 max.	Capacitor should be stored for 1 to 2 hrs. at room condition.			
		I.R.	3,000MΩ m	in	-			
		Dielectric Strength	Per Item 6					
		Appearance	No marked	defect				
		Capacitance change	C: Within ±2 S: Within ±5 X, B, E: Wit F: Within±1	5% hin ±10%				
		·	Char.	Specification	Apply the rated voltage for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity.			
13	Humidity Loading	tan δ or Q	C, S	$Q \ge 275+5/2C_R (C_R < 30pF)$ $Q \ge 350 (C_R \ge 30pF)$	Post-treatment:			
		an o or Q	X, B, E	tan δ: 0.050 max.	Capacitor should be stored for 1 to 2 hrs. at room condition.			
			F	tan δ: 0.075 max.	_			
		I.R.	3,000MΩ m	in				
		Dielectric Strength	Per Item 6					

continue ...





SPECIFICATION AND TEST METHOD (continue ...)

No.	lte	em	Specification	Testing Method			
		Appearance Capacitance change	No marked defect C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within±15% 3000MΩ min	Impulse Voltage: Each individual capacitor should be subjected to a 5kV (Type X1Y1: 8kV) impulses for three times. After the capacitors are applied to life test.			
14	Life Test	Dielectric Strength	Per Item 6	Apply a voltage of Table 4 for 1000 hrs. at 125+2/-0°C, and relative Humidity of 50% max. < Table 4 > Applied Voltage AC425V (r.m.s.), except that once each hour the Voltage is increased to AC1000V (r.m.s.) for 0.1 sec. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition			
15	5 Flame Test		The capacitor flame discontinues as follows. Cycle Time (sec.) 1 to 4 30 5 60	The capacitor should be subjected to applied flame for 15 sec. And then removed for 15 sec. until 5 cycles are completed. Capacitor Flame Gas Burner (in min)			
16	Robustness of Terminations Bending		Lead wire should not be cut off. Capacitor should not be broken.	Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec. Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90° bend in the opposite direction at the rate of one bend in 2 to 3 sec.			
17	Active Flammability		The cheese-cloth should not be on fire	The capacitor should be individually wrapped in at least one but not more than two complete layers of cheese-cloth. The capacitor should not be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 minutes after the last discharge. C1, 2: 1uF ± 10% C3: 0.33uF ± 5%, 10KV, Ct: 3uF ± 5%, 10KV, Cx: Capacitor under test F: Fuse, Rated 10A R: 100? ± 5% Ur: Rated Voltage Ut: Voltage applied to Ct. L1 to 4: 1.5mH ± 20%, 16A Rod core choke			
18	Passive Flammability		The burning time should not exceed 30 sec. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position which best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame: 30 sec. Length of flame: 12 ± 1mm Gas burner: Length 35mm min Inside Dia. 0.5 ± 0.1mm Outside Dia 0.9mm max. Gas: Butane gas Purity 95% min			



SPECIFICATION AND TEST METHOD (continue ...)

No.	Ite	em	Specification			Testin	g Metho	d
		Appearance	No marked defect			to 2 immersion c	ycles.	perature cycles, then
					< Temperature Cycle Charter Temperature (90)			
					Step	Temperature (°C)		Time (min)
	Temperature	Capacitance change	C: Within ±2.5% S: Within ±5%		1	-25+0/	'-3	30
			X, B, E: Within ±10%		2	Room temperature		3
			F: Within ±15%		3	125+3/-0		30
					4	Room temp	erature	3
		tan δ or Q				•	C	ycle time : 5 cycle
19	and Immersion		Per Item 4	< Immersion Cycle >				
	Cycle				Step	Temperature (°C)	Time (min)	Immersion Water
					1	65+5/-0	0±3	Clean water
		I.R.	3,000MΩ min		2	15	15	Salt water
							С	ycle time : 2 cycle
		Dielectric Strength	Per Item 6	Ca at Post	room col -treatmer	should be stored andition for 24±2 hint:	rs. before init	1hr., and then placed ial meaurements.

STORAGE ENVIRONMENT

Do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degree centigrade and 20 to 70%.