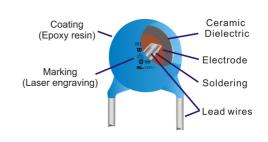




■ INTRODUCTION

- Complies with IEC60384-14
- Single Layer AC Disc Safety Capacitor
- Coated with flame-retardant epoxy resin (conforming to UL94V-0)
- Lead with tin plated copper wire, inert metal copper or silver as a coating after the electrode, see diagram
- Halogen-free available



■ RECOGNITIONS: made for RFE by UL/VDE/CQC shop Welson

Approved	Country	Standards	Rated '	√oltage	Certification	n Number	
monogram	Country	Standards	KL (X1Y2)	WD (X1Y1)	KL (X1Y2)	WD (X1Y1)	
c 911 us	USA	UL 60384-14	X1 : 500Vac	X1 : 760Vac	F404F70		
c 714 us	Canada	UL 60364-14	Y2 : 500Vac Y1 : 500Vac		E104572		
DYE 10	Germany	EN60384-14 (0565-1-1) : 2014-04 EN60384-14: 2013-08 IEC 60384-14 ed. 4	X1 : 440Vac Y2 : 300Vac			4016157	
	Korea	KC60384-14 / KC60384-1	250Vac		SZ03004-17002		
CGC	China	GB/T 6346.14-2015	X1 : 400Vac Y2 : 250Vac	X1 : 400Vac Y1 : 250Vac	CQC03001008380	CQC03001008379	

■ ELECTRICAL CHARACTERISTICS

Characteristics	WD type (X1 Y1)	KL type (X1 Y2)				
Capacitance range	1pF to 0.01uF	1pF to 0.015uF				
Rated voltage	X1: 760/660/400Vac; (1500 Vdc) Y1: 500/250Vac; (1500 Vdc)	X1: 500/440/400Vac; Y2: 500/300/250Vac				
Dielectric strength	4000Vac (50Hz-60Hz, 50mA max.) for 1 minute.	2500Vac (50Hz-60Hz, 50mA max.) for 1 minute.				
Capacitance (CR)	Within the specified tolerance. Y5P, Y5U, Y5V, X7R measured at 1kHz±20% C0G, SL measured at 1MHz±20% Both are 1Vrms, 25					
Dissipation Factor (tanδ) or Q Value		P, Y5U, X7R: Y5V: anδ: 0.025 max. tanδ: 0.050 max.				
Insulation resistance	10,000MΩ minimum at 500VDC for 1 minute.					
Operating temperature	-55°C to 125°C					





■ CAPACITANCE CHART

Class			X1 Y2	(KL Typ	e)				K1 Y1 (V	VD Type	2)	
Dielectric	C0G	SL	Y5P	X7R	Y5U	Y5V	C0G	SL	Y5P	X7R	Y5U	Y5V
Capacitance (pF)	(C)	(S)	(B)	(X)	(E)	(F)	(C)	(S)	(B)	(X)	(E)	(F)
1, 1.5, 2.2, 3.3	G	G	G				G	G	G			
4.7	G	G	G				J	J	- 1			
5.1	G	G	G	G			J	J	1	- 1		
6.8	J	J	ı				K	К	J			
10	J	G	ı	G			K	G	J	J		
15		G		G				G	J	J		
18		G	ı	G				G	J	J		
20		G		G				G	J	J		
22		G	1	G				G	J	J		
27		J	i	G				G	J	K		
30		J	i	G				G	J	K		
33		J	i	G				L	K	K		
36		J	i	G				L	K	K		
39		J	i	G				L	K	K		
47		K	l I	G				L	K	K	-	
56		K	K	G					K	K	G	
68		K	K	G					K	K	G	
82		K	K	G					K	K	G	
100			G	G					K	K	G	
120			G	G					K	K	G	
150			G	G					K	K	G	
180			G	G					K	K	G	
220			G	G					K	K	G	
270			G	G					K	K	G	G
330			G	G	E				K	K	G	G
390			J	- 1	Е				K	L	G	G
470			J	- 1	Е	E			L	L	- 1	G
560			J	I	Е	Е			L	L	ı	G
680			J	I	Е	Е			N	N	I	G
820			J	J	Е	Е			N	N	- 1	G
860			J	J	Е	Е			0	0	К	ı
1000			L	J	G	G			R	R	К	J
1200			L	М	G	G			R	R	L	J
1500			N	М	ı	ı			R	R	L	J
1800			N		i	i					L	K
2000			0		J	i					L	K
2200			0		J	ı					M	L
3000					L	J					0	M
3300					L	J					0	M
3600					M	K					Q	M
3900					M	K					Q	N
4700					N	L *					Q	N
5600					0	L★					S	Q
6800	ļ				0	M	 				S	R
8200					R	N					U	R
8600	ļ				S	N						
10000					S	0						
15000					U	Q						
Lead Spacing (F)	5.0 & 7.5 & 10.0											
(±1.0mm)	7.5 & 10.0											
Thickness (T)			6	.0mm					6.0	mm		
max. Capacitance												
Tolerance	Below	10pF:	±0.25	pF or ±0	0.50pF;	10pF~10	0pF: ±5	% or ±1	.0%; Ov	er 100p	F:±10%	or 209
	Epoxy Resin (UL94V-0)											
Coating					Fr	OVV Rec	in (1110	4\/ <u>-</u> ∩\				

KL103M W I @	
11 400 3000- 12 3301 3000- 12 301 3000-	
→ F →	lu

Code	Е	G	I	J	K	L	L★	М	N	0	Q	R	S	T	U
Diameter (D) max.	7mm	8mm	9mm	10mm	11mm	12mm	13mm	14mm	15mm	16mm	19mm	20mm	22mm	25mm	28mm





■ HOW TO ORDER

1. Type Code: KL class X1-Y2; WD class X1-Y1

2. Temperature Characteristic:

Code	Temp. Coefficient	Code	Temp. Coefficient	Code	Temp. Coefficient	
С	C0G	В	Y5P	Е	Y5U	
S	SL	Х	X7R	F	Y5V	

3. Capacitance Code:

Expressed by three-digit alphanumeric. The unit is pico-farad (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers. If there is a decimal point in between first two figures, it is expressed by the capital letter "R". See below examples:

Code	Capacitance	Code	Capacitance	Code	Capacitance
5R1	5.1 pF	100	10 pF	472	4700 pF
8R0	8 pF	101	100 pF	103	0.1 uF

4. Capacitance Tolerance Code

Code	Tolerance	Code	Tolerance	Code	Tolerance
С	± 0.25pF	J	± 5%	М	± 20%
D	± 0.50pF	K	± 10%	Z	+80/-20%

5. Minimum Test Voltage (AC): X1Y2 = AC2K5, X1Y1 = AC4KV

6. Lead Spacing (F)

Code	Lead spacing (F)	Code	Lead spacing (F)		
5	5.0±0.8mm	7	7.5±0.8mm		
6	6.35±0.8mm	10	10.0±0.8mm		

7. Lead style

Code	S	Υ	X	W	Z
Lead Style	D T	D T	D T	D T T	D d d

8. Lead length & package style:

Omitted for un-cut bulk pack or Code L

5 = 5 + /-1mm, bulk pack, 6 = 6 + /-1mm ... etc. (cut leads only available in bulk pack)

A = Taped & Ammo pack, R = Taped & on Reel



P/N

Ceramic Capacitors CLASS 5 Series: AC X1Y1 & X1Y2



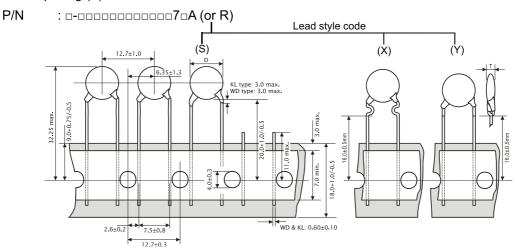
■ TAPING SPECIFICATION

Lead spacing (F): 5mm

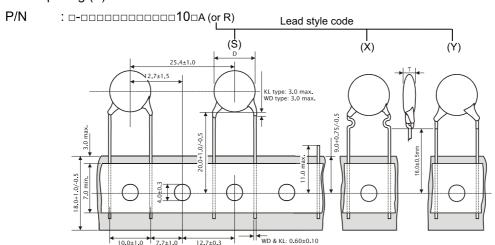
: a-aaaaaaaaa5aA (or R) Lead style code (S) (X) 6.35±1.3 32.25 max → | 0.52±0.10 4.0±0.3

12.7±0.3

Lead spacing (F): 7.5mm



Lead spacing (F): 10.0mm







■ SPECIFICATION AND TEST METHOD

- 1. Please measure with the ambient temperature of 25±2°C, relative humidity of 45~85 percent.
- 2. Please measure in this order: Capacitance, DF, IR, Test voltage
- 3. Measurement of voltage of high-voltage capacitors should be based on 150V/µs rate, rise from 0V voltage. The test of time can be increased to test the voltage start time. Measuring the end of the capacitor should discharge.

No.	Ite	em		Specification		Те	sting Meth	od			
1	Appeara Dimer			efect on appearance form ns are within specified range.			citor should be visually inspected for evidence of defect ns should be measured with slide calipers.				
2	Mar	king	To be easily le	egible		The capacito	r should be visua	ally inspected			
3	Capacita	nce (CR)	Within specified tolerance								
4	Dissipation Factor (tan δ) or Q Value		Char. C, S X, B, E F	Specification $Q \ge 400 + 20 \text{CR } (\text{CR} < 30 \text{pF})$ $Q \ge 1000 \qquad (\text{CR} \ge 30 \text{pF})$ $\tan \delta : 0.050 \text{ max.}$ $\tan \delta : 0.075 \text{ max.}$	The capacitor and dissipation factor should be measured at 25±1°C with 1±0.2KHz (char. C & L: 1±0.2MHz) and AC5V(r.m.s.) max.						
5	Insulation I		10,000MΩ mir	n.	within 60		ng. The voltage	ure with DC500±50V should be applied to			
		Between Lead Wires	No failure		Table 1	acitor should not lare applied betwee /Discharge curren Type Test Voltage	en the lead wire	en test voltages of s for 60 sec. WD AC4000V			
6	Dielectric Strength	Body Insulation	No failure	No failure		e terminals of the ed together. Then a metal foil should around the body stance of about 3 th terminal. e capacitor should into a container fills of about 1 mm AC voltage of Tabor 60 sec. between lead wires and r	, as shown in fig d be closely of the capacitor to 4mm d be illed with diameter. le 2 is en the	jure W			
							< Table 2 >				
						Туре	KL	WD			
						Test Voltage	AC2500V	AC4000V			
			Char.	Capacitance Change	The can	acitance measur	ement should be	e made at each step			
			В	Within ± 10%		d in Table 3.					
			×	Within ± 15%			< Table 3 >				
			E	Within +20% / -55%		Step	Temperatu	re (°C)			
7		erature	F	Within +30% / -80%		1	20 ± 2	, ,			
'	Charac	teristics	(Tem	np range: -25 to +85°C)		2	-25 ± 1				
			Char.	Temperature Coefficient		3	20 ± 2				
			C	0±30ppm/°C		4	85 ± 2				
			S	+350 to -1000ppm/°C		5	20 ± 2				
			(Tem	np range: -25 to +85°C)							

continue ...





■ SPECIFICATION AND TEST METHOD (continue ...)

No.	Ite	m	Specification	Testing Method			
8	Solder of Le		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder (Pb37/Sn63) 235±5°C			
		Appearance Capacitance	No marked defect C, S: ±5% or 1pF, whichever is larger	The lead wires should be immersed in solder of 350±10°C or 260±5°C up to 1.5mm to 2.0mm from the root of terminal for 3.5±0.5 sec. (10±1 sec for 260±5°C)			
9	Soldering Effect (Non-Preheat)	change	B: ±10% X, E, F: ±20%	Pre-treatment: Capacitor should be stored at 85±2°C for 1hr., and then placed			
		Dielectric Strength	Per Item 6	at room condition for 24±2 hrs. before initial meaurements. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.			
		Appearance	No marked defect				
10	Soldering Capacitance change		C, S: ±5% or 1pF, whichever is larger B: ±10% X, E, F: ±20%	Capacitor should be stored at 120+0/-5°C for 60+0/-5 sec. The lead wires should be immersed in solder of 260±5°C up to 1.5mm to 2.0mm from the root of terminal for 7.5+0/-1 sec.			
	(On-Preheat)	I.R.	1,000MΩ min	Pre-treatment and Post-treatment: see per Item 9			
		Dielectric Strength	Per Item 6				
		Appearance	No marked defect	The capacitor should be firmly soldered to the supporting lead			
11	1 Vibration Resistance	Capacitance	Within the specified tolerance	wire and vibrated at a frequency range of 10Hz to 55Hz,1.5mm in total amplitude, with about a 1 minute rate of vibration change from 10Hz to 55 Hz and back to 10Hz. Apply for a total of 6 hrs.,			
		tan δ or Q	Per Item 4	2 hrs each in 3 mutually perpendicular directions.			
		Appearance	No marked defect				
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within ±15%				
12	Humidity (Under Steady State)	tan δ or Q	Char. Specification C, S Q ≥ 275+5/2CR (CR < 30pF)	Set the capacitor for 500±12 hrs., at 40±2°C in 90 to 95% relative humidity. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.			
			F tan δ: 0.075 max.	_			
		I.R.	3,000MΩ min				
		Dielectric Strength	Per Item 6				
		Appearance	No marked defect				
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within±15%				
	Lium: dite.		Char. Specification	Apply the rated voltage for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity.			
13	Humidity Loading	tan δ or Q	C, S $Q \ge 275+5/2C_R (C_R < 30pF)$ $Q \ge 350 (C_R \ge 30pF)$	Post-treatment:			
		.a., 0 01 Q	X, B, E tan δ: 0.050 max.	Capacitor should be stored for 1 to 2 hrs. at room condition.			
			F tan δ: 0.075 max.	_			
		I.R.	3,000MΩ min				
		Dielectric Strength	Per Item 6	continue			

continue ...





■ SPECIFICATION AND TEST METHOD (continue ...)

No.	Item		Specification	Testing Method			
14	Life Test	Appearance Capacitance change	X, B, E: Within ±10% F: Within±15%	Impulse Voltage: Each individual capacitor should be subjected to a 5kV (Type X1Y1: 8kV) impulses for three times. After the capacitors are applied to life test.			
		Dielectric Strength	3000MΩ min Per Item 6	Apply a voltage of Table 4 for 1000 hrs. at 125+2/-0°C, and relati Humidity of 50% max. < Table 4 > Applied Voltage AC425V (r.m.s.), except that once each hour the Voltage is increased to AC1000V (r.m.s.) for 0.1 sec. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition			
15	Flame Test		The capacitor flame discontinues as follows. Cycle Time (sec.) 1 to 4 30 5 60	The capacitor should be subjected to applied flame for 15 sec. And then removed for 15 sec. until 5 cycles are completed. Capacitor Flame Gas Burner (in mm)			
16	Robustness of Terminations	Tensile	Lead wire should not be cut off.	Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec.			
		Bending	Capacitor should not be broken.	Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90° bend in the opposite direction at the rate of one bend in 2 to 3 sec.			
17	Active Flammability		The cheese-cloth should not be on fire	The capacitor should be individually wrapped in at least one but not more than two complete layers of cheese-cloth. The capacitor should not be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 minutes after the last discharge.			
18	Passive FI	ammability	The burning time should not exceed 30 sec. The tissue paper should not ignite.	Ut : Voltage applied to Ct. L1 to 4: 1.5mH ± 20%, 16A Rod core choke The capacitor under test should be held in the flame in the position which best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame: 30 sec. Length of flame: 12 ± 1mm Gas burner: Length 35mm min Inside Dia. 0.5 ± 0.1mm Outside Dia 0.9mm max. Gas: Butane gas Purity 95% min			

continue ...





■ SPECIFICATION AND TEST METHOD (continue ...)

No.	ltem		Specification	Testing Method						
	Temperature and Immersion Cycle	Appearance	No marked defect	The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. < Temperature Cycle >						
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within ±15% Per Item 4	-	Step	Temperature (°C)		Time (min)		
				_	1	-25+0/-3		30		
				_	2	Room temperature		3		
					3	125+3/-0		30		
				<u> </u>	4	Room temperature		3		
19		tan δ or Q			Cycle time : 5 cycle < Immersion Cycle > Step Temperature (°C) (min) Immersion Water					
		I.R.	3,000MΩ min	1 -	1	65+5/-0	0±3	Clean water		
				_	2	15	15	Salt water		
				Cycle time : 2 cycle Pre-treatment:						
		Dielectric Strength	Per Item 6	Capacitor should be stored at 85±2°C for 1hr., and then placed at room condition for 24±2 hrs. before initial meaurements. Post-treatment: Capacitor should be stored for 24±2 hrs. at room condition.						

■ STORAGE ENVIRONMENT

Do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degree centigrade and 20 to 70%.