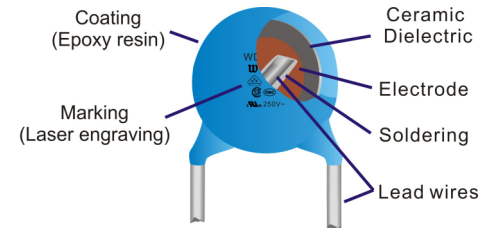


INTRODUCTION

- Complies with IEC60384-14
- Single Layer AC Disc Safety Capacitor
- Coated with flame-retardant epoxy resin (conforming to UL94V-0)
- Lead with tin plated copper wire, inert metal copper or silver as a coating after the electrode, see diagram
- Halogen-free available



RECOGNITIONS : made for RFE by UL/VDE/CQC shop Welson

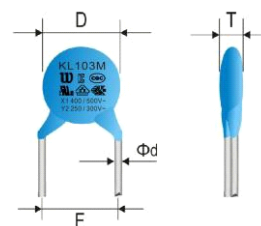
Approved monogram	Country	Standards	Rated Voltage		Certification Number	
			KL (X1Y2)	WD (X1Y1)	KL (X1Y2)	WD (X1Y1)
	USA	UL 60384-14	X1 : 500Vac	X1 : 760Vac	E104572	
	Canada		Y2 : 500Vac	Y1 : 500Vac		
	Germany	EN60384-14 (0565-1-1) : 2014-04 EN60384-14: 2013-08 IEC 60384-14 ed. 4	X1 : 440Vac Y2 : 300Vac	X1 : 660Vac Y1 : 500Vac	40016156	4016157
	Korea	KC60384-14 / KC60384-1	250Vac		SZ03004-17002	SZ03004-17001
	China	GB/T 6346.14-2015	X1 : 400Vac Y2 : 250Vac	X1 : 400Vac Y1 : 250Vac	CQC03001008380	CQC03001008379

ELECTRICAL CHARACTERISTICS

Characteristics	WD type (X1 Y1)	KL type (X1 Y2)
Capacitance range	1pF to 0.01uF	1pF to 0.015uF
Rated voltage	X1: 760/660/400Vac; (1500 Vdc) Y1: 500/250Vac; (1500 Vdc)	X1: 500/440/400Vac; Y2: 500/300/250Vac
Dielectric strength	4000Vac (50Hz-60Hz, 50mA max.) for 1 minute.	2500Vac (50Hz-60Hz, 50mA max.) for 1 minute.
Capacitance (CR)	Within the specified tolerance. Y5P, Y5U, Y5V, X7R measured at 1kHz±20% ----- C0G, SL measured at 1MHz±20% Both are 1Vrms, 25	
Dissipation Factor (tanδ) or Q Value	SL and C0G: Q ≥ 400+20CR (CR < 30pF) Q ≥ 1000 (CR ≥ 30pF) Measured condition see "Capacitance"	Y5P, Y5U, X7R: tanδ: 0.025 max. Y5V: tanδ: 0.050 max.
Insulation resistance	10,000MΩ minimum at 500VDC for 1 minute.	
Operating temperature	-55°C to 125°C	

■ CAPACITANCE CHART

Class	X1 Y2 (KL Type)						X1 Y1 (WD Type)					
Dielectric Capacitance (pF)	C0G (C)	SL (S)	Y5P (B)	X7R (X)	Y5U (E)	Y5V (F)	C0G (C)	SL (S)	Y5P (B)	X7R (X)	Y5U (E)	Y5V (F)
	1, 1.5, 2.2, 3.3	G	G	G			G	G	G			
4.7	G	G	G				J	J	I			
5.1	G	G	G	G			J	J	I	I		
6.8	J	J	I				K	K	J			
10	J	G	I	G			K	G	J	J		
15		G	I	G				G	J	J		
18		G	I	G				G	J	J		
20		G	I	G				G	J	J		
22		G	I	G				G	J	J		
27		J	I	G				G	J	K		
30		J	I	G				G	J	K		
33		J	I	G				L	K	K		
36		J	I	G				L	K	K		
39		J	I	G				L	K	K		
47		K	I	G				L	K	K		
56		K	K	G					K	K	G	
68		K	K	G					K	K	G	
82		K	K	G					K	K	G	
100			G	G					K	K	G	
120			G	G					K	K	G	
150			G	G					K	K	G	
180			G	G					K	K	G	
220			G	G					K	K	G	
270			G	G					K	K	G	G
330			G	G	E				K	K	G	G
390			J	I	E				K	L	G	G
470			J	I	E	E			L	L	I	G
560			J	I	E	E			L	L	I	G
680			J	I	E	E			N	N	I	G
820			J	J	E	E			N	N	I	G
860			J	J	E	E			O	O	K	I
1000			L	J	G	G			L★	O	K	J
1200			L	M	G	G			N	Q	L	J
1500			N	M	I	I			O	R	L	J
1800			N		I	I					L	K
2000			O		J	I					L	K
2200					J	I					M	L
3000					L	J					O	M
3300					L	J					O	M
3600					M	K					Q	M
3900					M	K					Q	N
4700					N	L★					Q	N
5600					O	L★					S	Q
6800					O	M					S	R
8200					R	N					U	R
8600					S	N						
10000					S	O						
15000					U	Q						
Lead Spacing (F) (±1.0mm)	5.0 & 7.5 & 10.0						7.5 & 10.0					
Thickness (T) max.	6.0mm						6.0mm					
Capacitance Tolerance	Below 10pF: ±0.25pF or ±0.50pF; 10pF~100pF: ±5% or ±10%; Over 100pF:±10% or 20%											
Coating	Epoxy Resin (UL94V-0)											
Body Color	Blue											



Code	E	G	I	J	K	L	L★	M	N	O	Q	R	S	T	U
Diameter (D) max.	7mm	8mm	9mm	10mm	11mm	12mm	13mm	14mm	15mm	16mm	19mm	20mm	22mm	25mm	28mm

■ HOW TO ORDER

5 - KL F 472 M AC2K5 10 Y 5
 (1) (2) (3) (4) (5) (6) (7) (8)

1. Type Code: KL class X1-Y2 ; WD class X1-Y1

2. Temperature Characteristic:

Code	Temp. Coefficient	Code	Temp. Coefficient	Code	Temp. Coefficient
C	C0G	B	Y5P	E	Y5U
S	SL	X	X7R	F	Y5V

3. Capacitance Code:

Expressed by three-digit alphanumeric. The unit is pico-farad (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers. If there is a decimal point in between first two figures, it is expressed by the capital letter "R". See below examples:

Code	Capacitance	Code	Capacitance	Code	Capacitance
5R1	5.1 pF	100	10 pF	472	4700 pF
8R0	8 pF	101	100 pF	103	0.1 uF

4. Capacitance Tolerance Code

Code	Tolerance	Code	Tolerance	Code	Tolerance
C	± 0.25pF	J	± 5%	M	± 20%
D	± 0.50pF	K	± 10%	Z	+80/-20%

5. Minimum Test Voltage (AC) : X1Y2 = AC2K5, X1Y1 = AC4KV

6. Lead Spacing (F)

Code	Lead spacing (F)	Code	Lead spacing (F)
5	5.0±0.8mm	7	7.5±0.8mm
6	6.35±0.8mm	10	10.0±0.8mm

7. Lead style

Code	S	Y	X	W	Z
Lead Style					

D	T	S	d	L	F
Disc Diameter	Disc Thickness	Seating Plane to Component (5.0mm max.)	Wire Diameter	Uncut Bulk Pack Lead Length	Lead Space

8. Lead length & package style:

Omitted for un-cut bulk pack or Code L

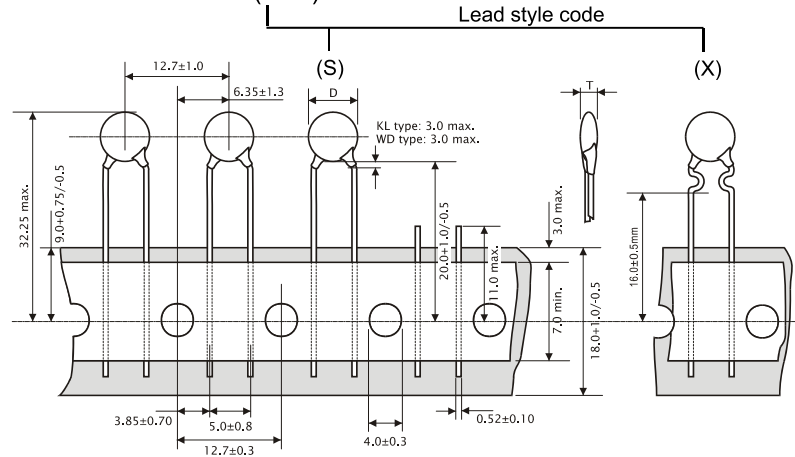
5 = 5+/-1mm, bulk pack, 6 = 6+/-1mm ... etc. (cut leads only available in bulk pack)

A = Taped & Ammo pack, R = Taped & on Reel

■ TAPING SPECIFICATION

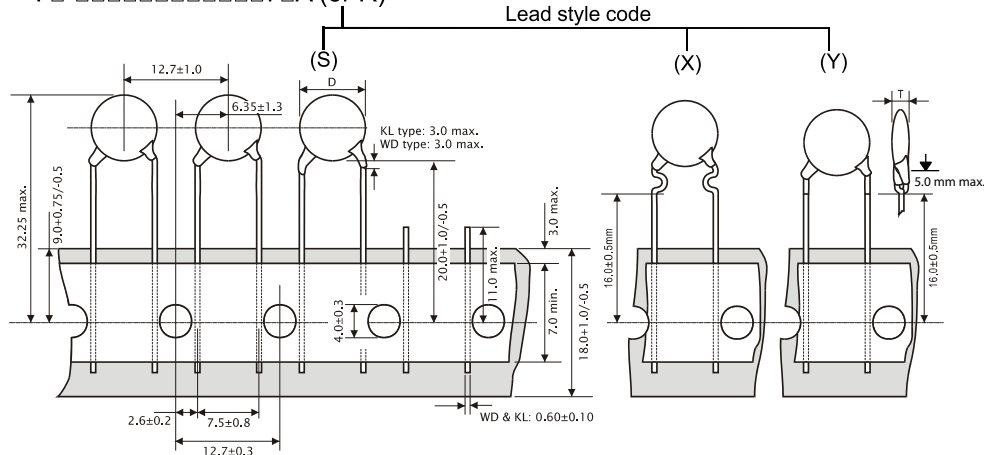
Lead spacing (F): 5mm

P/N : □-□□□□□□□□□□5□A (or R)



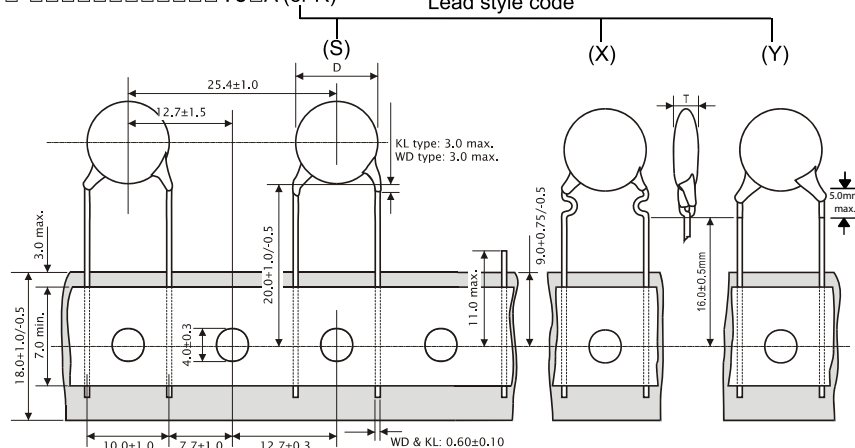
Lead spacing (F): 7.5mm

P/N : □-□□□□□□□□□□7□A (or R)



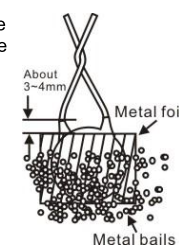
Lead spacing (F): 10.0mm

P/N : □-□□□□□□□□□□10□A (or R)



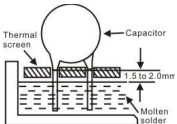
■ SPECIFICATION AND TEST METHOD

1. Please measure with the ambient temperature of $25 \pm 2^\circ\text{C}$, relative humidity of 45~85 percent.
2. Please measure in this order: Capacitance, DF, IR, Test voltage
3. Measurement of voltage of high-voltage capacitors should be based on $150\text{V}/\mu\text{s}$ rate, rise from 0V voltage. The test of time can be increased to test the voltage start time. Measuring the end of the capacitor should discharge.

No.	Item		Specification	Testing Method																														
1	Appearance and Dimensions		No marked defect on appearance form and dimensions are within specified range.	The capacitor should be visually inspected for evidence of defect. Dimensions should be measured with slide calipers.																														
2	Marking		To be easily legible	The capacitor should be visually inspected																														
3	Capacitance (CR)		Within specified tolerance	The capacitor and dissipation factor should be measured at 25±1°C with 1±0.2KHz (char. C & L: 1±0.2MHz) and AC5V(r.m.s.) max.																														
4	Dissipation Factor (tan δ) or Q Value	Char.	Specification																															
		C, S	Q ≥ 400+20CR (CR < 30pF) Q ≥ 1000 (CR ≥ 30pF)																															
		X, B, E	tan δ: 0.050 max.																															
		F	tan δ: 0.075 max.																															
5	Insulation Resistance (I.R.)		10,000MΩ min.	The insulation resistance should be measure with DC500±50V within 60±5 sec. of charging. The voltage should be applied to the capacitor through a resistor of 1MΩ.																														
6	Dielectric Strength	Between Lead Wires	No failure	The capacitor should not be damaged when test voltages of Table 1 are applied between the lead wires for 60 sec. (Charge/Discharge current =50mA) <table><tr><th colspan="3">< Table 1 ></th></tr><tr><th>Type</th><th>KL</th><th>WD</th></tr><tr><td>Test Voltage</td><td>AC2500V</td><td>AC4000V</td></tr></table>	< Table 1 >			Type	KL	WD	Test Voltage	AC2500V	AC4000V																					
		< Table 1 >																																
Type	KL	WD																																
Test Voltage	AC2500V	AC4000V																																
		Body Insulation	No failure	First, the terminals of the capacitor should be connected together. Then, as shown in figure at right, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 4mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC voltage of Table 2 is applied for 60 sec. between the capacitor lead wires and metal balls.  <table><tr><th colspan="3">< Table 2 ></th></tr><tr><th>Type</th><th>KL</th><th>WD</th></tr><tr><td>Test Voltage</td><td>AC2500V</td><td>AC4000V</td></tr></table>	< Table 2 >			Type	KL	WD	Test Voltage	AC2500V	AC4000V																					
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Type	KL	WD																																
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7	Temperature Characteristics		<table><tr><th>Char.</th><th>Capacitance Change</th></tr><tr><td>B</td><td>Within ± 10%</td></tr><tr><td>X</td><td>Within ± 15%</td></tr><tr><td>E</td><td>Within +20% / -55%</td></tr><tr><td>F</td><td>Within +30% / -80%</td></tr></table> (Temp range: -25 to +85°C) <table><tr><th>Char.</th><th>Temperature Coefficient</th></tr><tr><td>C</td><td>0±30ppm/°C</td></tr><tr><td>S</td><td>+350 to -1000ppm/°C</td></tr></table> (Temp range: -25 to +85°C)	Char.	Capacitance Change	B	Within ± 10%	X	Within ± 15%	E	Within +20% / -55%	F	Within +30% / -80%	Char.	Temperature Coefficient	C	0±30ppm/°C	S	+350 to -1000ppm/°C	The capacitance measurement should be made at each step specified in Table 3. <table><tr><th colspan="2">< Table 3 ></th></tr><tr><th>Step</th><th>Temperature (°C)</th></tr><tr><td>1</td><td>20 ± 2</td></tr><tr><td>2</td><td>-25 ± 2</td></tr><tr><td>3</td><td>20 ± 2</td></tr><tr><td>4</td><td>85 ± 2</td></tr><tr><td>5</td><td>20 ± 2</td></tr></table>	< Table 3 >		Step	Temperature (°C)	1	20 ± 2	2	-25 ± 2	3	20 ± 2	4	85 ± 2	5	20 ± 2
Char.	Capacitance Change																																	
B	Within ± 10%																																	
X	Within ± 15%																																	
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3	20 ± 2																																	
4	85 ± 2																																	
5	20 ± 2																																	

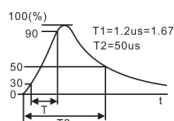
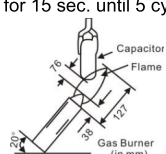
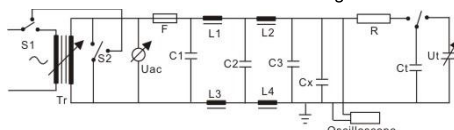
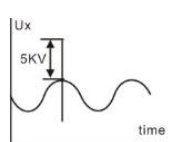
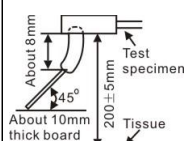
continue ...

■ SPECIFICATION AND TEST METHOD (continue ...)

No.	Item		Specification	Testing Method								
8	Solderability of Leads		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) 245±5°C H63 Eutectic Solder (Pb37/Sn63) 235±5°C								
9	Soldering Effect (Non-Preheat)	Appearance	No marked defect	The lead wires should be immersed in solder of 350±10°C or 260±5°C up to 1.5mm to 2.0mm from the root of terminal for 3.5±0.5 sec. (10±1 sec for 260±5°C)  Pre-treatment: Capacitor should be stored at 85±2°C for 1hr., and then placed at room condition for 24±2 hrs. before initial measurements. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.								
		Capacitance change	C, S: ±5% or 1pF, whichever is larger B: ±10% X, E, F: ±20%									
		I.R.	1,000MΩ min									
		Dielectric Strength	Per Item 6									
10	Soldering Effect (On-Preheat)	Appearance	No marked defect	Capacitor should be stored at 120+0/-5°C for 60+0/-5 sec. The lead wires should be immersed in solder of 260±5°C up to 1.5mm to 2.0mm from the root of terminal for 7.5+0/-1 sec. Pre-treatment and Post-treatment: see per Item 9								
		Capacitance change	C, S: ±5% or 1pF, whichever is larger B: ±10% X, E, F: ±20%									
		I.R.	1,000MΩ min									
		Dielectric Strength	Per Item 6									
11	Vibration Resistance	Appearance	No marked defect	The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10Hz to 55Hz, 1.5mm in total amplitude, with about a 1 minute rate of vibration change from 10Hz to 55 Hz and back to 10Hz. Apply for a total of 6 hrs., 2 hrs each in 3 mutually perpendicular directions.								
		Capacitance	Within the specified tolerance									
		tan δ or Q	Per Item 4									
12	Humidity (Under Steady State)	Appearance	No marked defect	Set the capacitor for 500±12 hrs., at 40±2°C in 90 to 95% relative humidity. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.								
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within ±15%									
		tan δ or Q	<table><tr><th>Char.</th><th>Specification</th></tr><tr><td>C, S</td><td>$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)</td></tr><tr><td>X, B, E</td><td>tan δ: 0.050 max.</td></tr><tr><td>F</td><td>tan δ: 0.075 max.</td></tr></table>		Char.	Specification	C, S	$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)	X, B, E	tan δ: 0.050 max.	F	tan δ: 0.075 max.
			Char.		Specification							
			C, S		$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)							
			X, B, E		tan δ: 0.050 max.							
F	tan δ: 0.075 max.											
I.R.	3,000MΩ min											
Dielectric Strength	Per Item 6											
13	Humidity Loading	Appearance	No marked defect	Apply the rated voltage for 500±12 hrs. at 40±2°C in 90 to 95% relative humidity. Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition.								
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within ±15%									
		tan δ or Q	<table><tr><th>Char.</th><th>Specification</th></tr><tr><td>C, S</td><td>$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)</td></tr><tr><td>X, B, E</td><td>tan δ: 0.050 max.</td></tr><tr><td>F</td><td>tan δ: 0.075 max.</td></tr></table>		Char.	Specification	C, S	$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)	X, B, E	tan δ: 0.050 max.	F	tan δ: 0.075 max.
			Char.		Specification							
			C, S		$Q \geq 275 + 5/2C_R$ ($C_R < 30\text{pF}$) $Q \geq 350$ ($C_R \geq 30\text{pF}$)							
			X, B, E		tan δ: 0.050 max.							
		F	tan δ: 0.075 max.									
I.R.	3,000MΩ min											
Dielectric Strength	Per Item 6											

continue ...

■ SPECIFICATION AND TEST METHOD (continue ...)

No.	Item		Specification	Testing Method						
14	Life Test	Appearance	No marked defect	Impulse Voltage: Each individual capacitor should be subjected to a 5kV (Type X1Y1: 8kV) impulses for three times. After the capacitors are applied to life test.  Apply a voltage of Table 4 for 1000 hrs. at 125±2/-0°C, and relative Humidity of 50% max. < Table 4 > <table><tr><th>Applied Voltage</th></tr><tr><td>AC425V (r.m.s.), except that once each hour the Voltage is increased to AC1000V (r.m.s.) for 0.1 sec.</td></tr></table> Post-treatment: Capacitor should be stored for 1 to 2 hrs. at room condition	Applied Voltage	AC425V (r.m.s.), except that once each hour the Voltage is increased to AC1000V (r.m.s.) for 0.1 sec.				
		Applied Voltage								
		AC425V (r.m.s.), except that once each hour the Voltage is increased to AC1000V (r.m.s.) for 0.1 sec.								
		Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within±15%							
I.R.	3000MΩ min									
Dielectric Strength	Per Item 6									
15	Flame Test		The capacitor flame discontinues as follows. <table><tr><th>Cycle</th><th>Time (sec.)</th></tr><tr><td>1 to 4</td><td>30</td></tr><tr><td>5</td><td>60</td></tr></table>	Cycle	Time (sec.)	1 to 4	30	5	60	The capacitor should be subjected to applied flame for 15 sec. And then removed for 15 sec. until 5 cycles are completed. 
Cycle	Time (sec.)									
1 to 4	30									
5	60									
16	Robustness of Terminations	Tensile	Lead wire should not be cut off. Capacitor should not be broken.	Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec. 						
		Bending		Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90° bend in the opposite direction at the rate of one bend in 2 to 3 sec.						
17	Active Flammability		The cheese-cloth should not be on fire	The capacitor should be individually wrapped in at least one but not more than two complete layers of cheese-cloth. The capacitor should not be subjected to 20 discharges. The interval between successive discharges should be 5 sec. The UAC should be maintained for 2 minutes after the last discharge.  C1, 2 : 1uF ± 10% C3 : 0.33uF ± 5%, 10KV, Ct : 3uF ± 5%, 10KV Cx : Capacitor under test F : Fuse, Rated 10A R : 100? ± 5% Ur : Rated Voltage Ut : Voltage applied to Ct. L1 to 4 : 1.5mH ± 20%, 16A Rod core choke 						
18	Passive Flammability		The burning time should not exceed 30 sec. The tissue paper should not ignite.	The capacitor under test should be held in the flame in the position which best promotes burning. Each specimen should only be exposed once to the flame. Time of exposure to flame : 30 sec.  Length of flame : 12 ± 1mm Gas burner : Length 35mm min Inside Dia. 0.5 ± 0.1mm Outside Dia 0.9mm max. Gas : Butane gas Purity 95% min						

continue ...

■ SPECIFICATION AND TEST METHOD (continue ...)

No.	Item		Specification	Testing Method																											
19	Temperature and Immersion Cycle	Appearance	No marked defect	The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. < Temperature Cycle > <table><tr><th>Step</th><th>Temperature (°C)</th><th>Time (min)</th></tr><tr><td>1</td><td>-25+0/-3</td><td>30</td></tr><tr><td>2</td><td>Room temperature</td><td>3</td></tr><tr><td>3</td><td>125+3/-0</td><td>30</td></tr><tr><td>4</td><td>Room temperature</td><td>3</td></tr></table> Cycle time : 5 cycle < Immersion Cycle > <table><tr><th>Step</th><th>Temperature (°C)</th><th>Time (min)</th><th>Immersion Water</th></tr><tr><td>1</td><td>65+5/-0</td><td>0±3</td><td>Clean water</td></tr><tr><td>2</td><td>15</td><td>15</td><td>Salt water</td></tr></table> Cycle time : 2 cycle Pre-treatment: Capacitor should be stored at 85±2°C for 1hr., and then placed at room condition for 24±2 hrs. before initial measurements. Post-treatment: Capacitor should be stored for 24±2 hrs. at room condition.	Step	Temperature (°C)	Time (min)	1	-25+0/-3	30	2	Room temperature	3	3	125+3/-0	30	4	Room temperature	3	Step	Temperature (°C)	Time (min)	Immersion Water	1	65+5/-0	0±3	Clean water	2	15	15	Salt water
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Capacitance change	C: Within ±2.5% S: Within ±5% X, B, E: Within ±10% F: Within ±15%																														
tan δ or Q	Per Item 4																														
I.R.	3,000MΩ min																														
Dielectric Strength	Per Item 6																														

■ STORAGE ENVIRONMENT

Do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Store the capacitors where the temperature and relative humidity do not exceed 5 to 40 degree centigrade and 20 to 70%.